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Docket No.: 57454-946

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

Customer Number: 20277

Hideto HIDAKA, et al.

Confirmation Number: 7552

Serial No.: 10/600,650

Group Art Unit: 2814

Filed: June 23, 2003

Examiner: P. Cao

For:

SEMICONDUCTOR DEVICE USING AN SOI SUBSTRATE

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This Information Disclosure Statement is being filed under Rule 37 CFR 1.97(i), wherein applicants are submitting references before the grant of a patent to be placed in the file.

Accordingly, copies of the references as listed on the attached Form PTO-1449 are submitted herewith for placement in the file. No certification or fees are deemed necessary.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOITT WILL & EMERY LLP

Stephen A. Becker Registration No. 26,527

600 13th Street, N.W. Washington, DC 20005-3096 202.756.8000 SAB:aph Facsimile: 202.756.8087

Date: September 2, 2004

SHEET 1 OF 1

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APPLICATION				APPLICANT Hideto HIDAKA, et al.					
(PTO-1449)				FILING DATE	FILING DATE GROUP			* * *	
				June 23, 2003 2814					
U.S. PATENT DOCUMENTS									
EXAMINER'S CIT	E	Document Number	Publication Date	Name of Patentee or Appli			Lines, Where		
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EXAMINER'S	F	oreign Patent Document	Name of Patentee or	Pages, Colu	mns.	Tra	nslation		
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.